



Power HIL Simulator (SimP)

A prototype to develop a high bandwidth interface

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Presentation overview

> SimP at a glance

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- > **SimP at a glance**
- > **Interface issue**

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- > **Prototype design:**
 - Power amplifier
 - Controller
 - Simulator

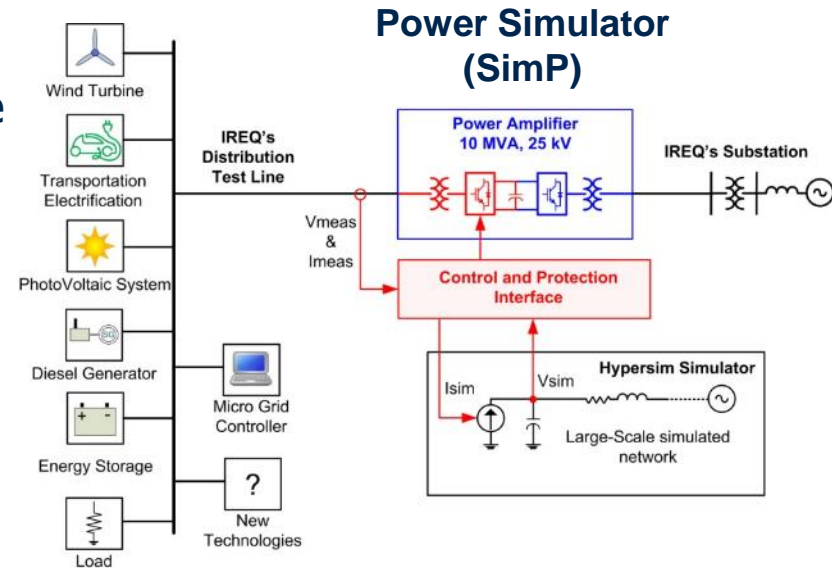
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- > **Conclusion**

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- Research & Testing Infrastructure for the validation of simulation models and for studying the dynamic behavior of electrical equipments connected to their power system.
- Scope: Smart Grid, Energy Storage, Renewable Energy Integration, ground transportation electrification



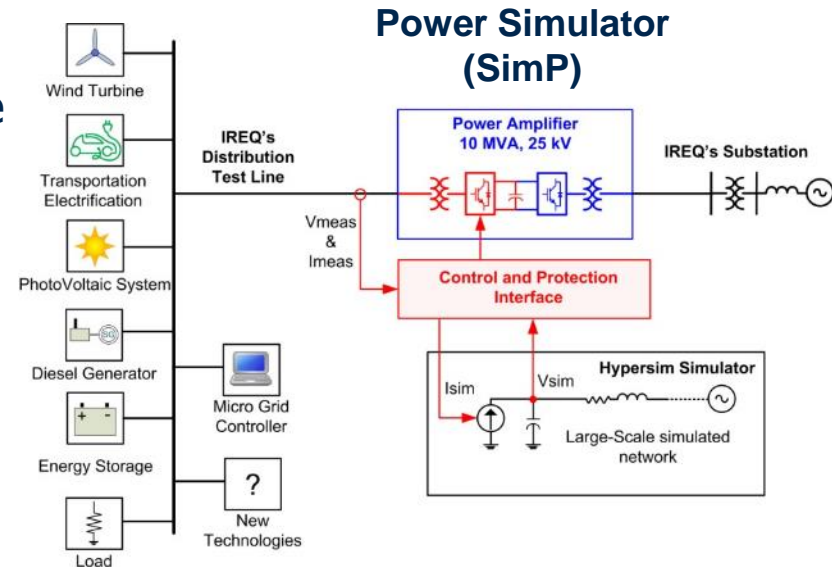
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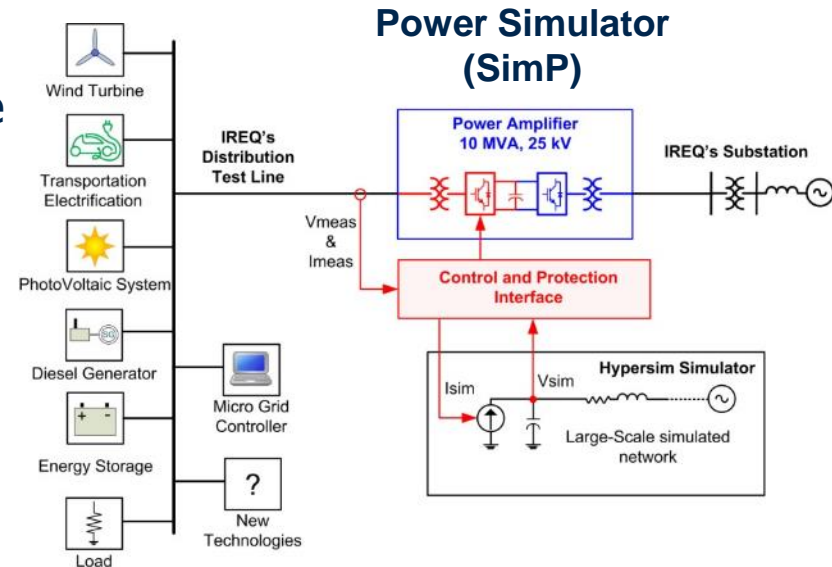
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> Project Outcomes

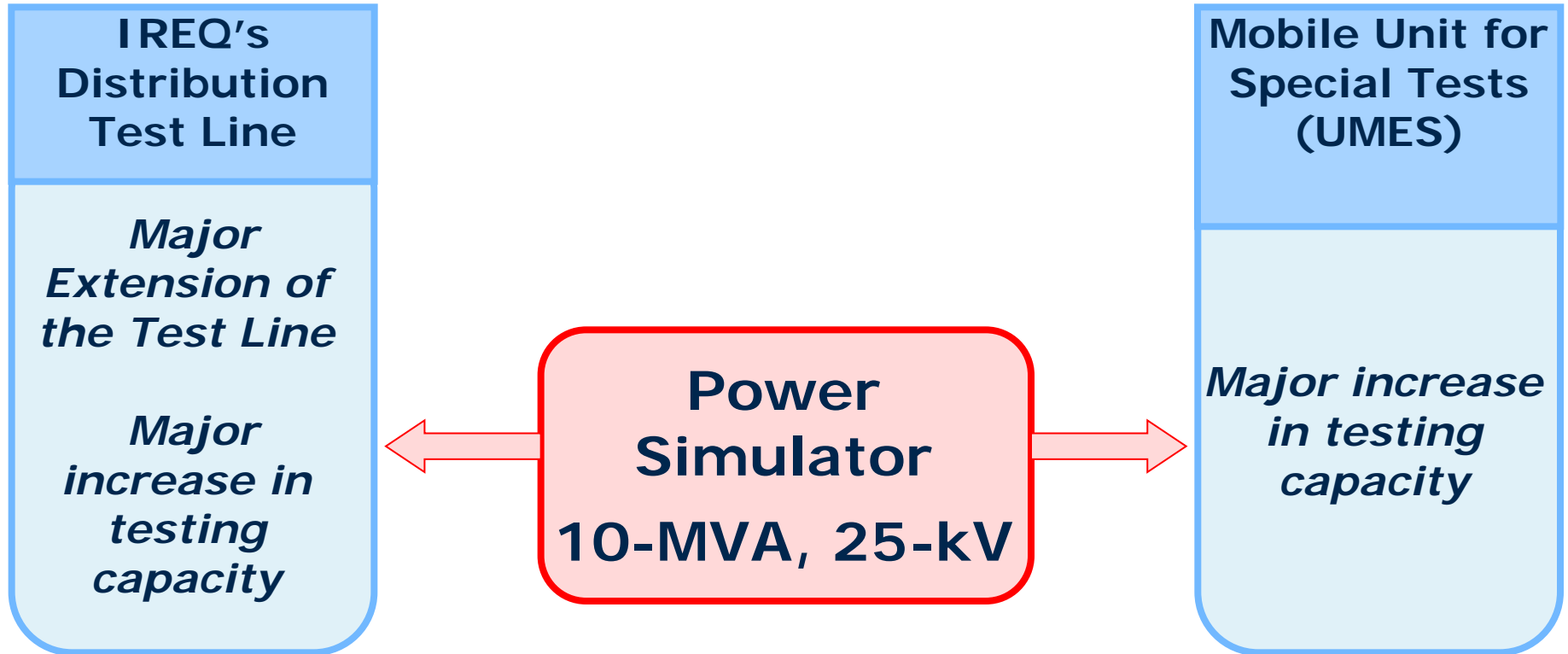
- World-class equipment
- Important extension of the IREQ's test line
- Major increase in testing capability
- Possibility of collaborations and partnerships on various projects



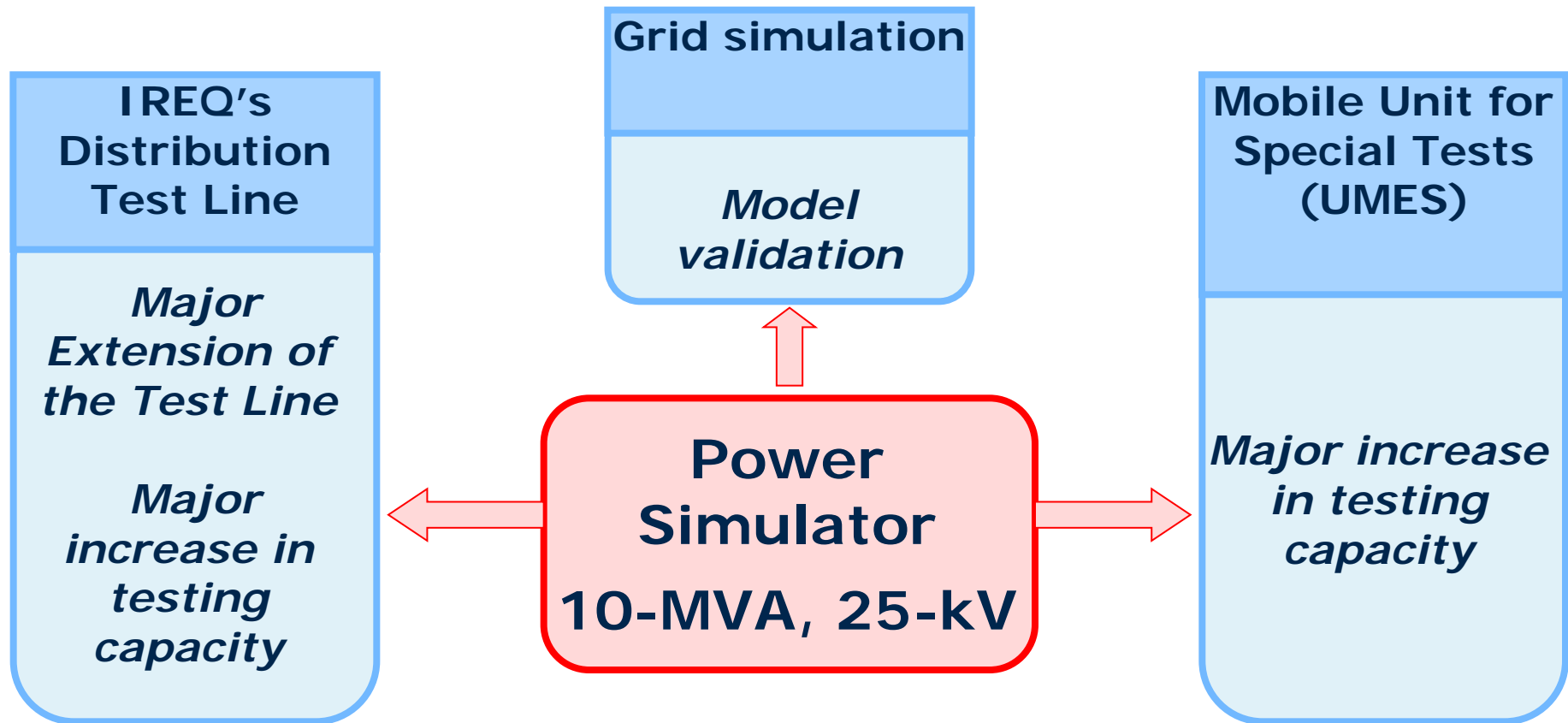
Context

**Power
Simulator
10-MVA, 25-kV**

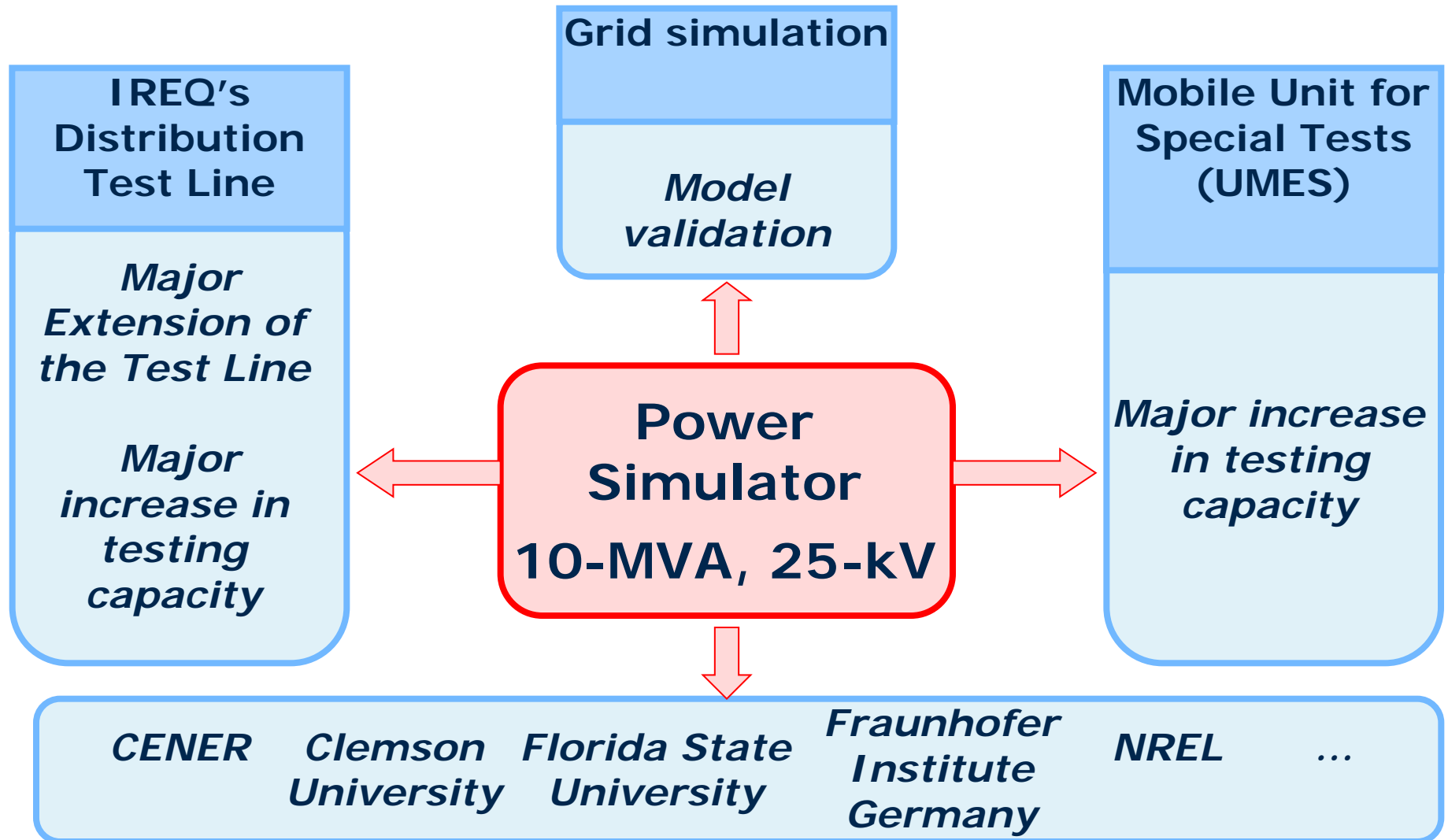
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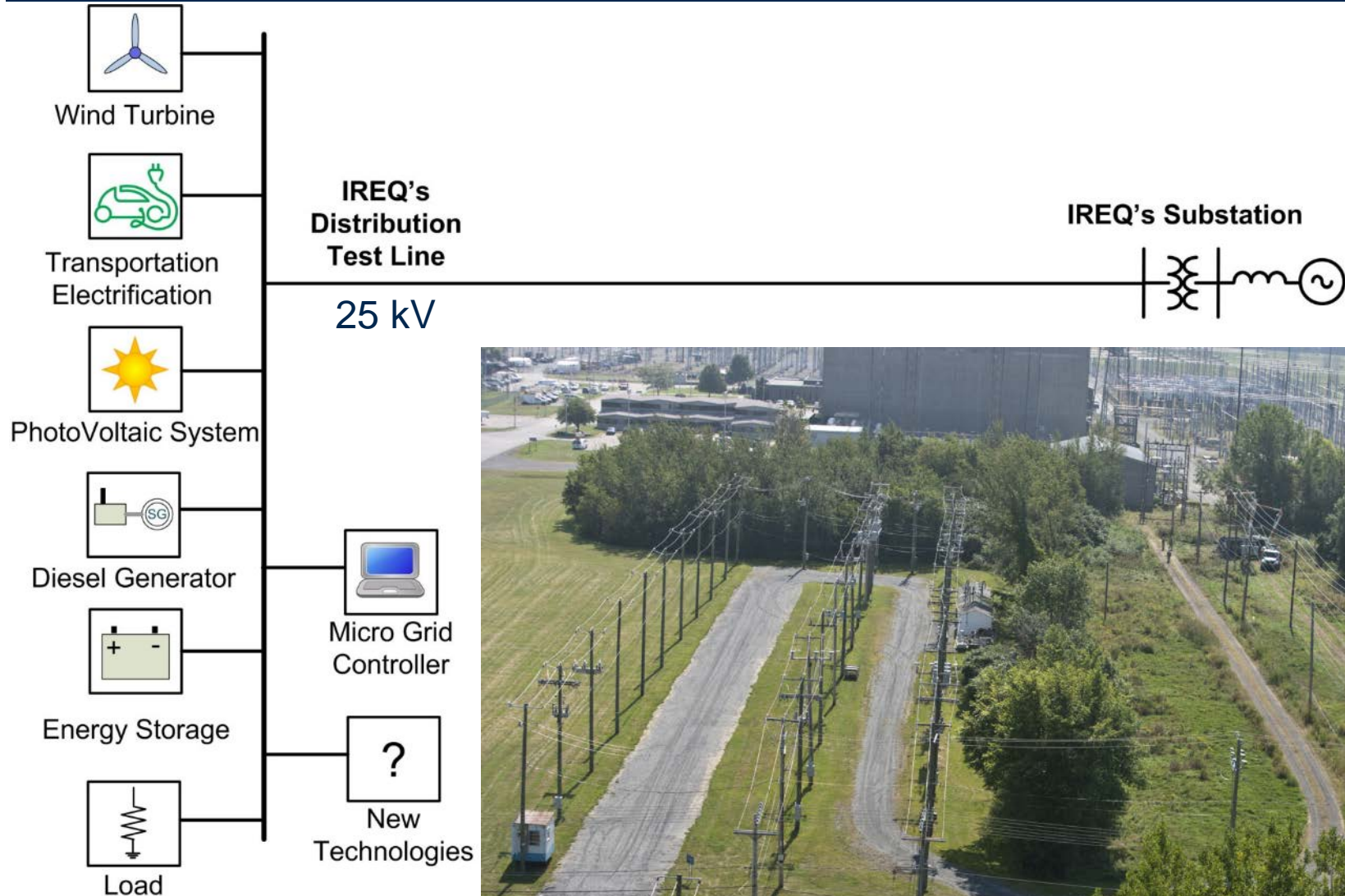
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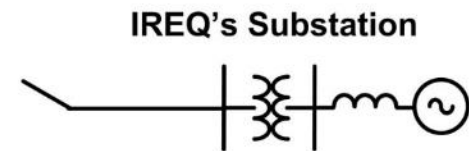
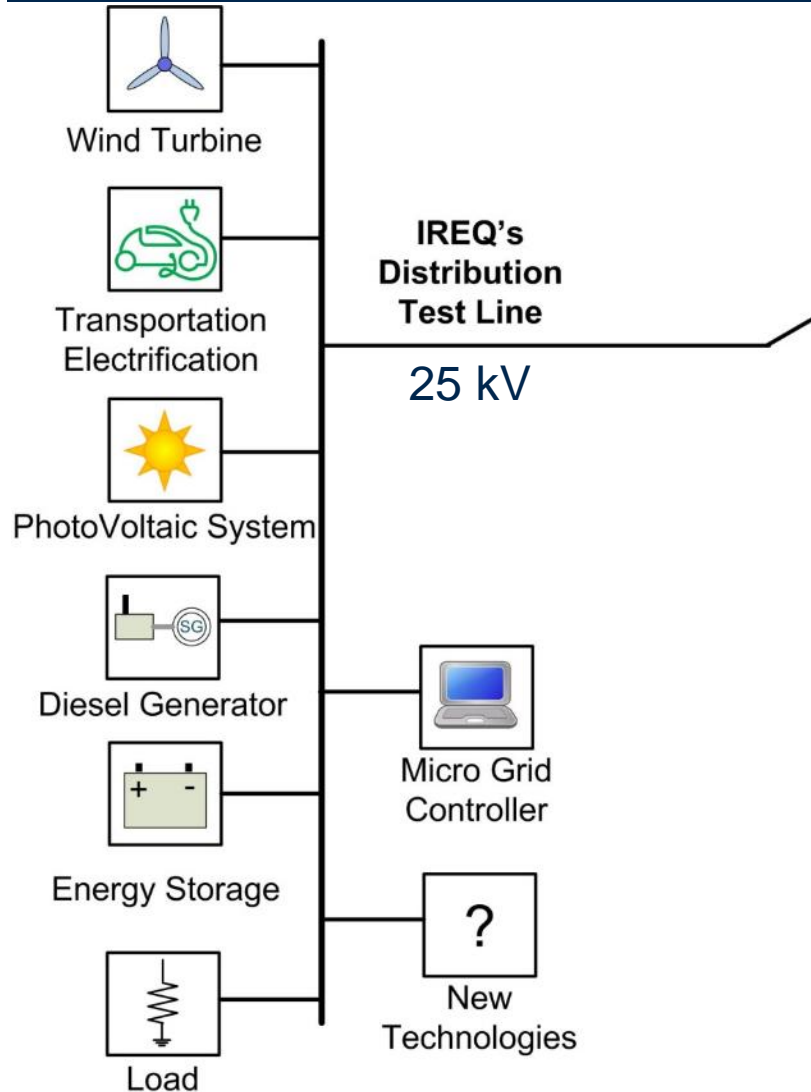
What is it?



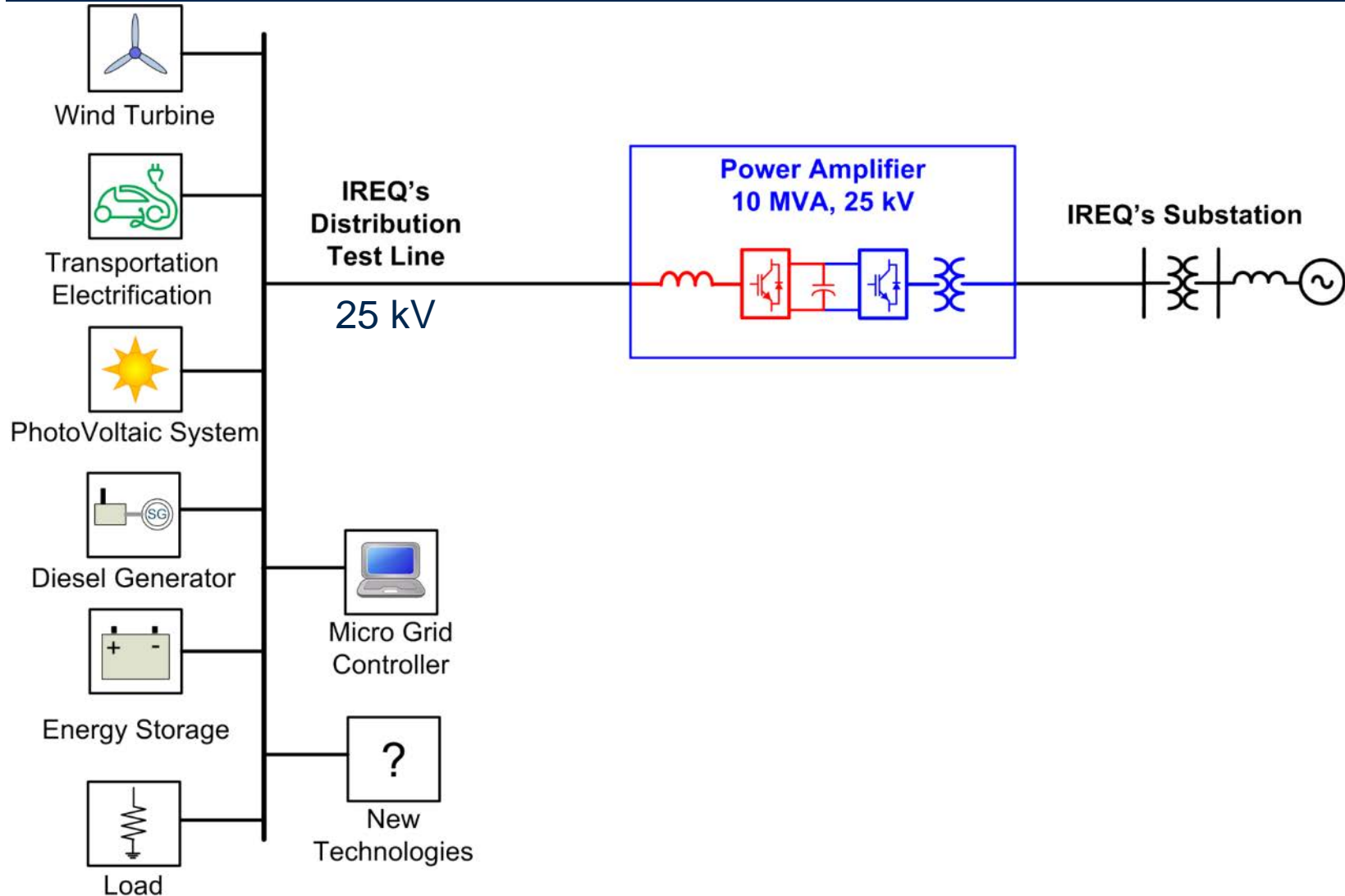
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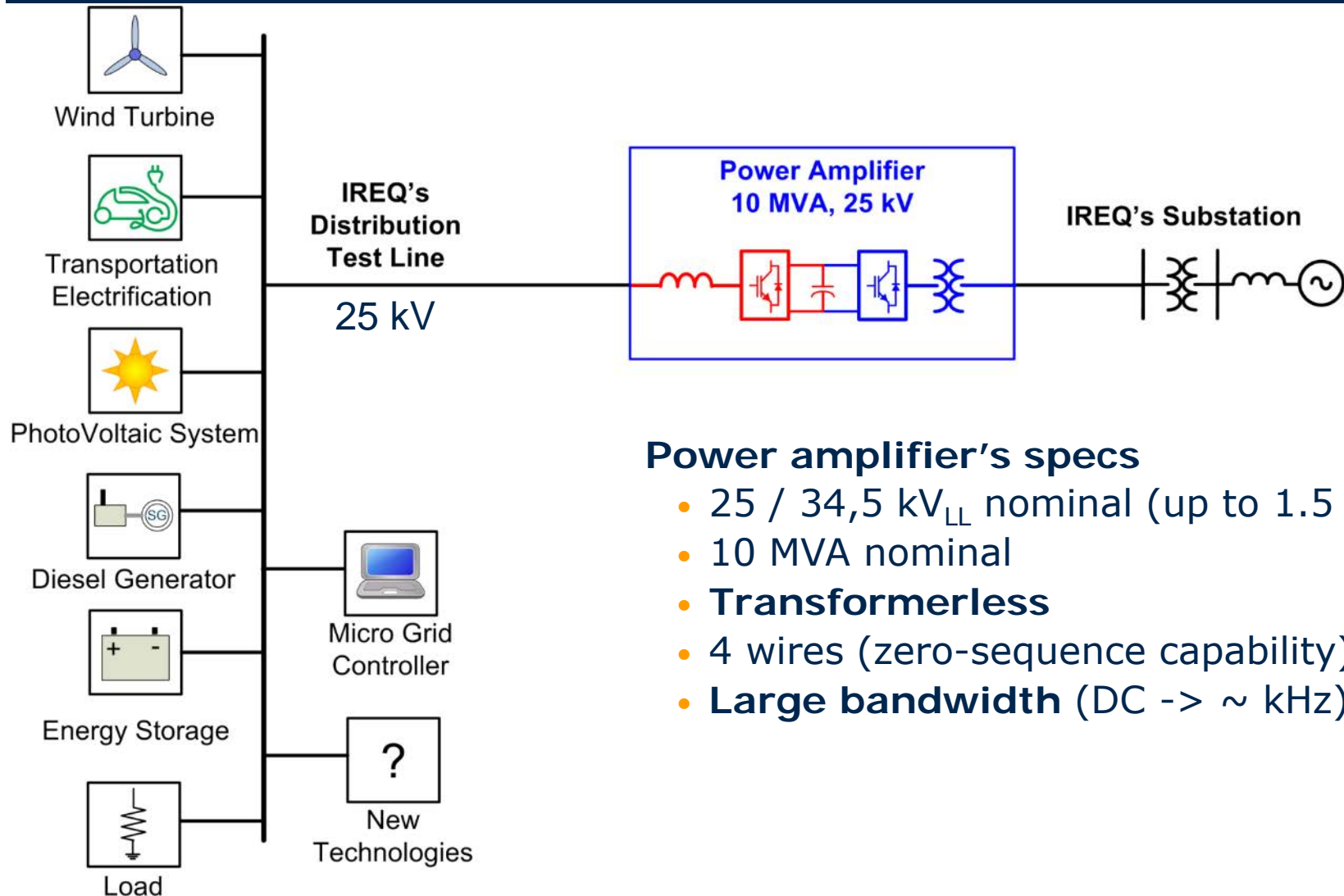
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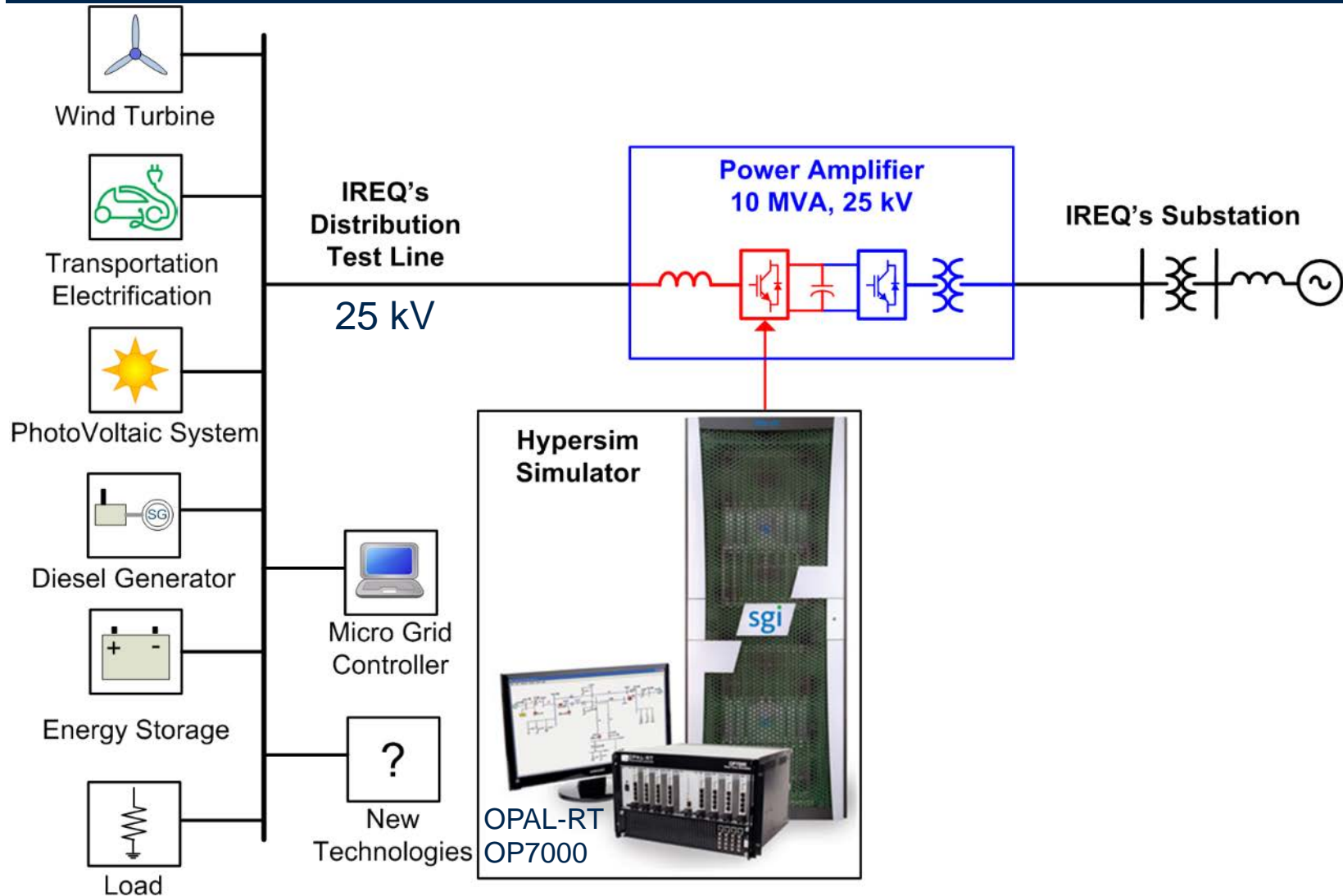
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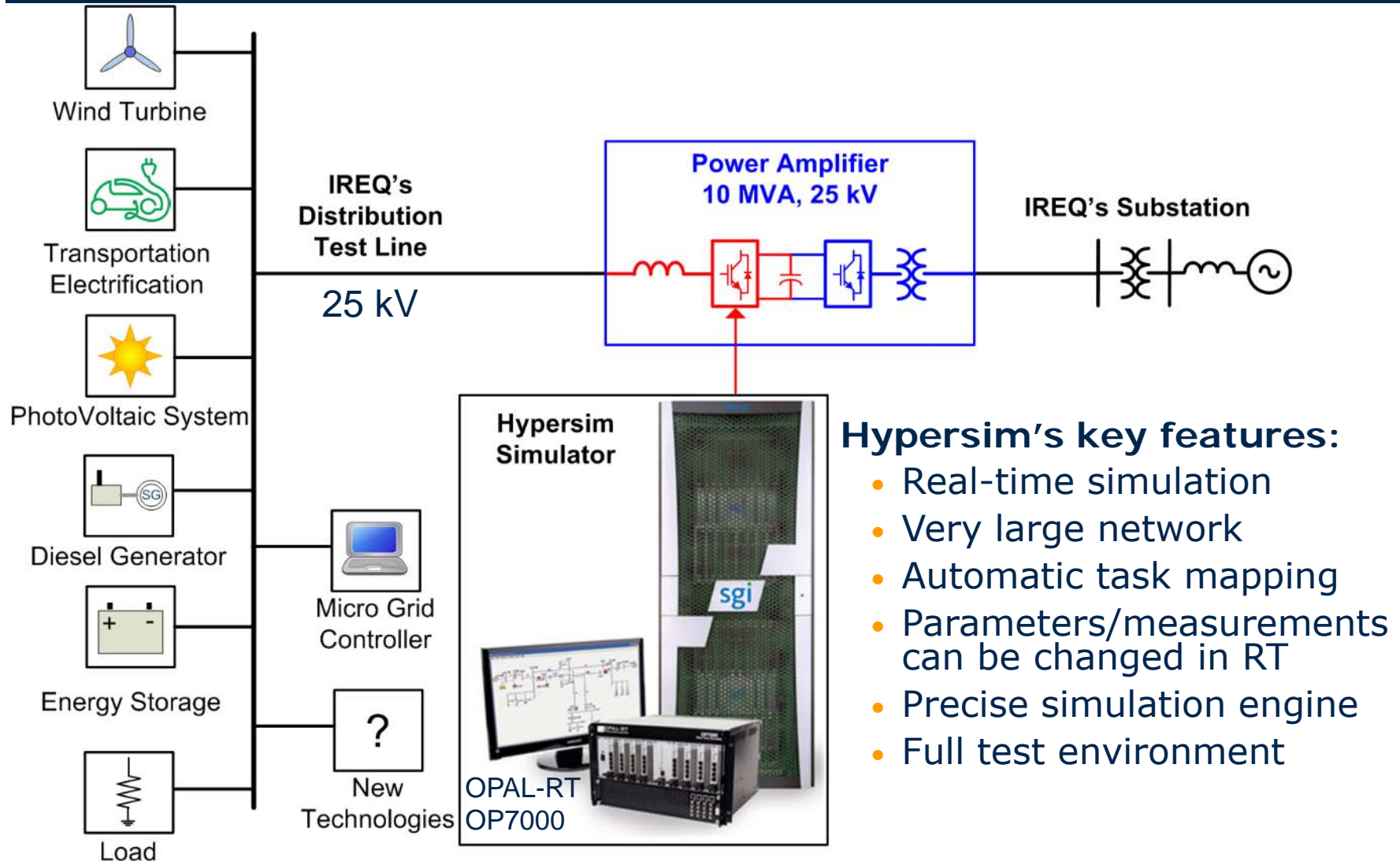
Power amplifier's specs

- 25 / 34,5 kV_{LL} nominal (up to 1.5 PU)
- 10 MVA nominal
- **Transformerless**
- 4 wires (zero-sequence capability)
- **Large bandwidth** (DC -> ~ kHz)

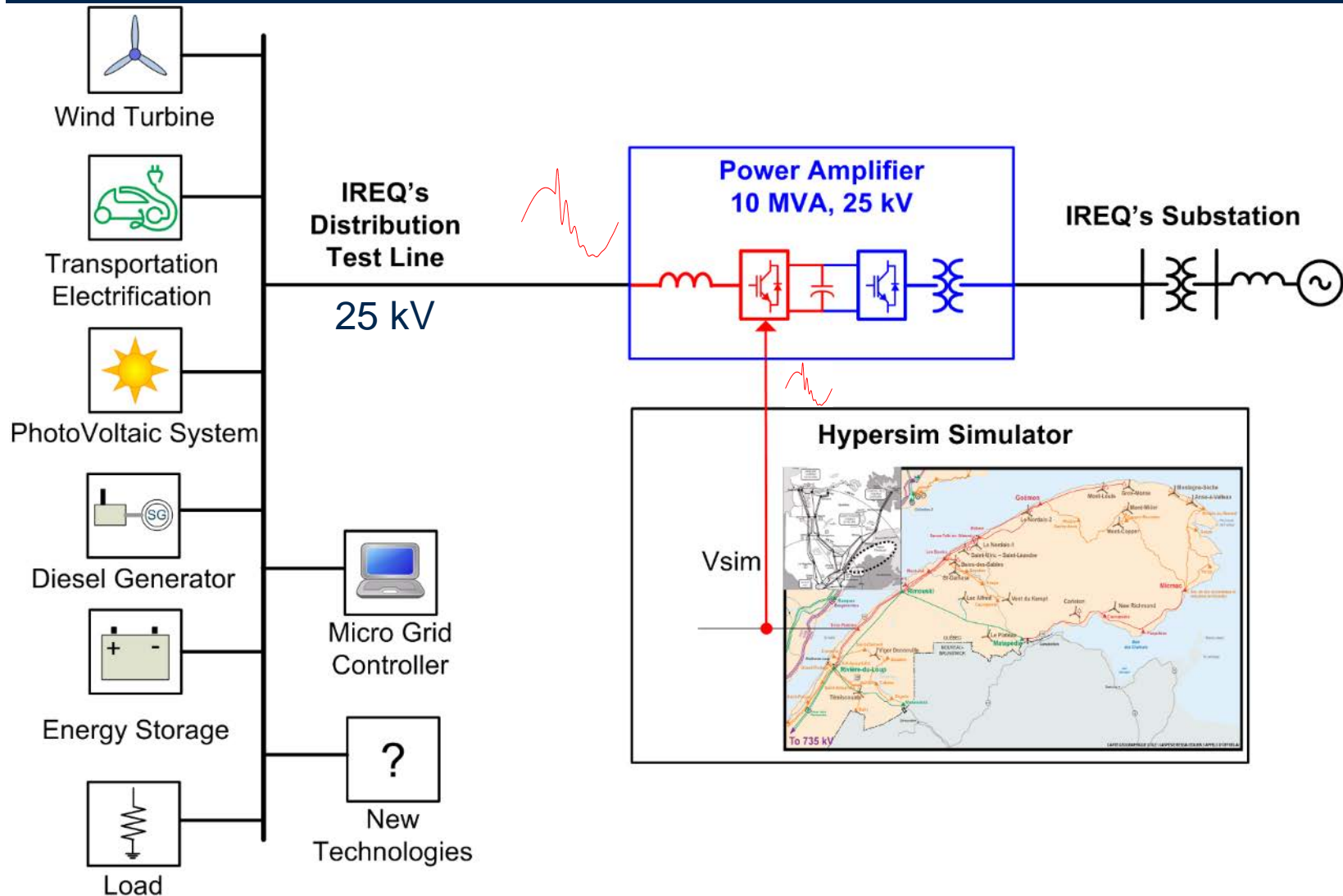
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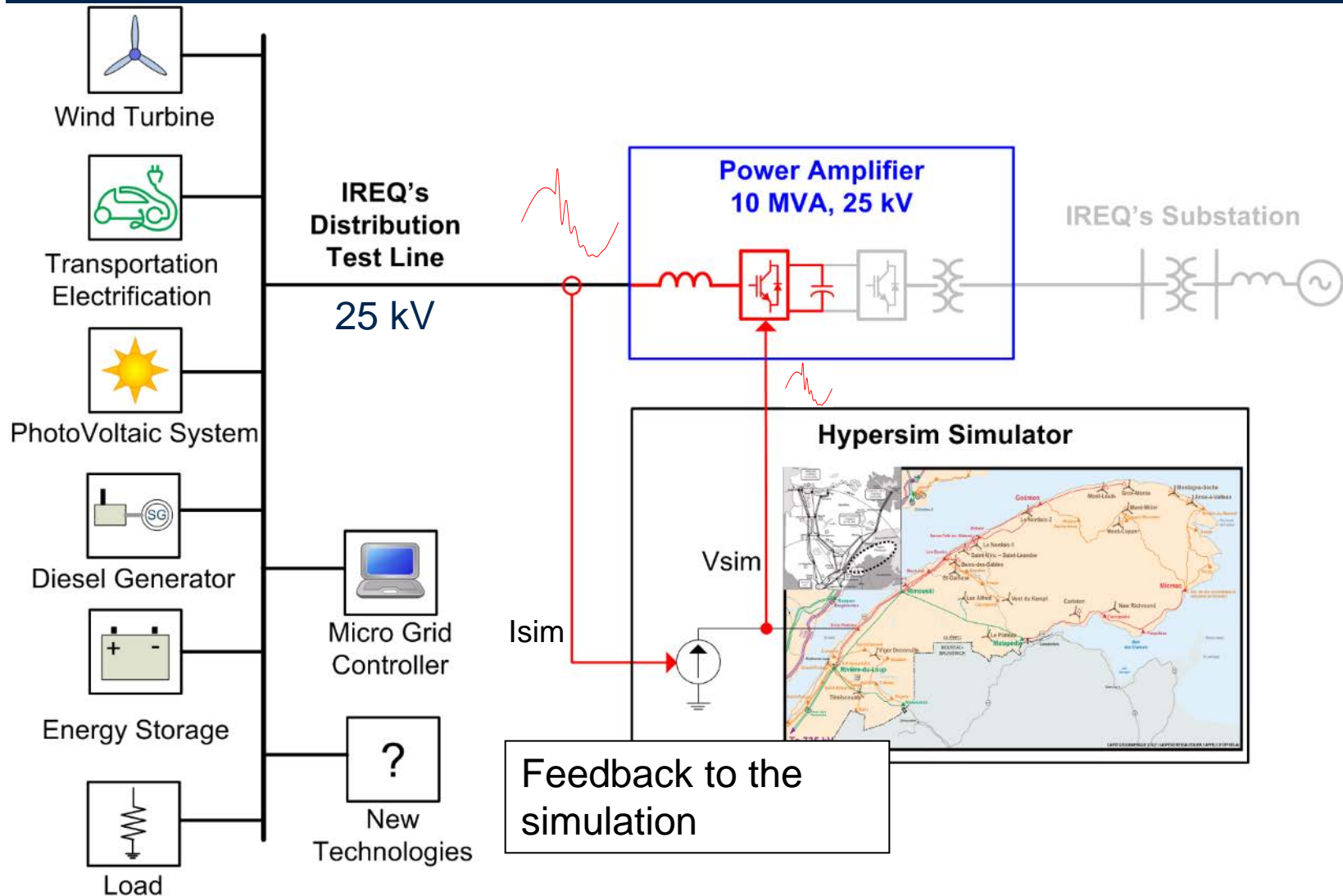
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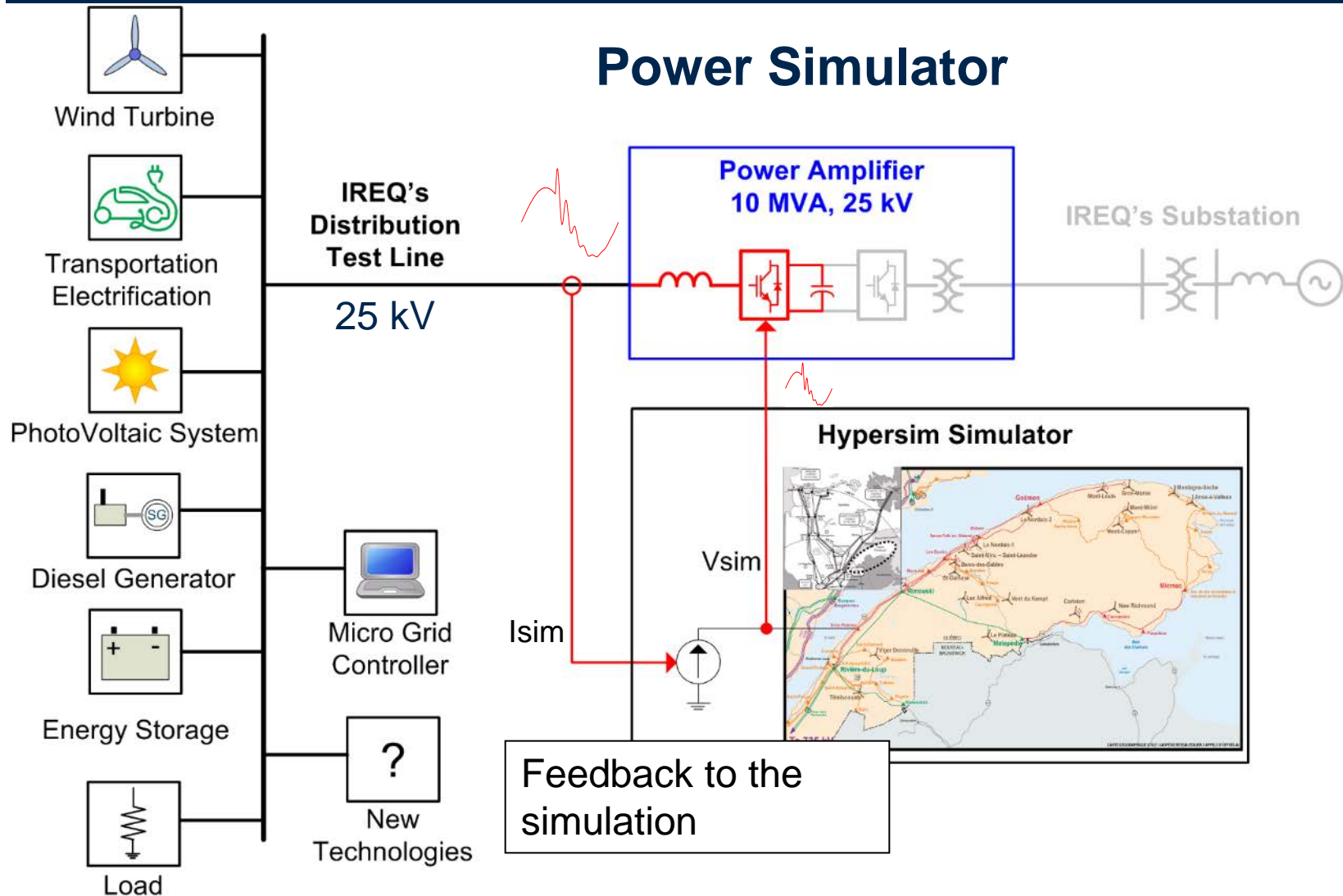
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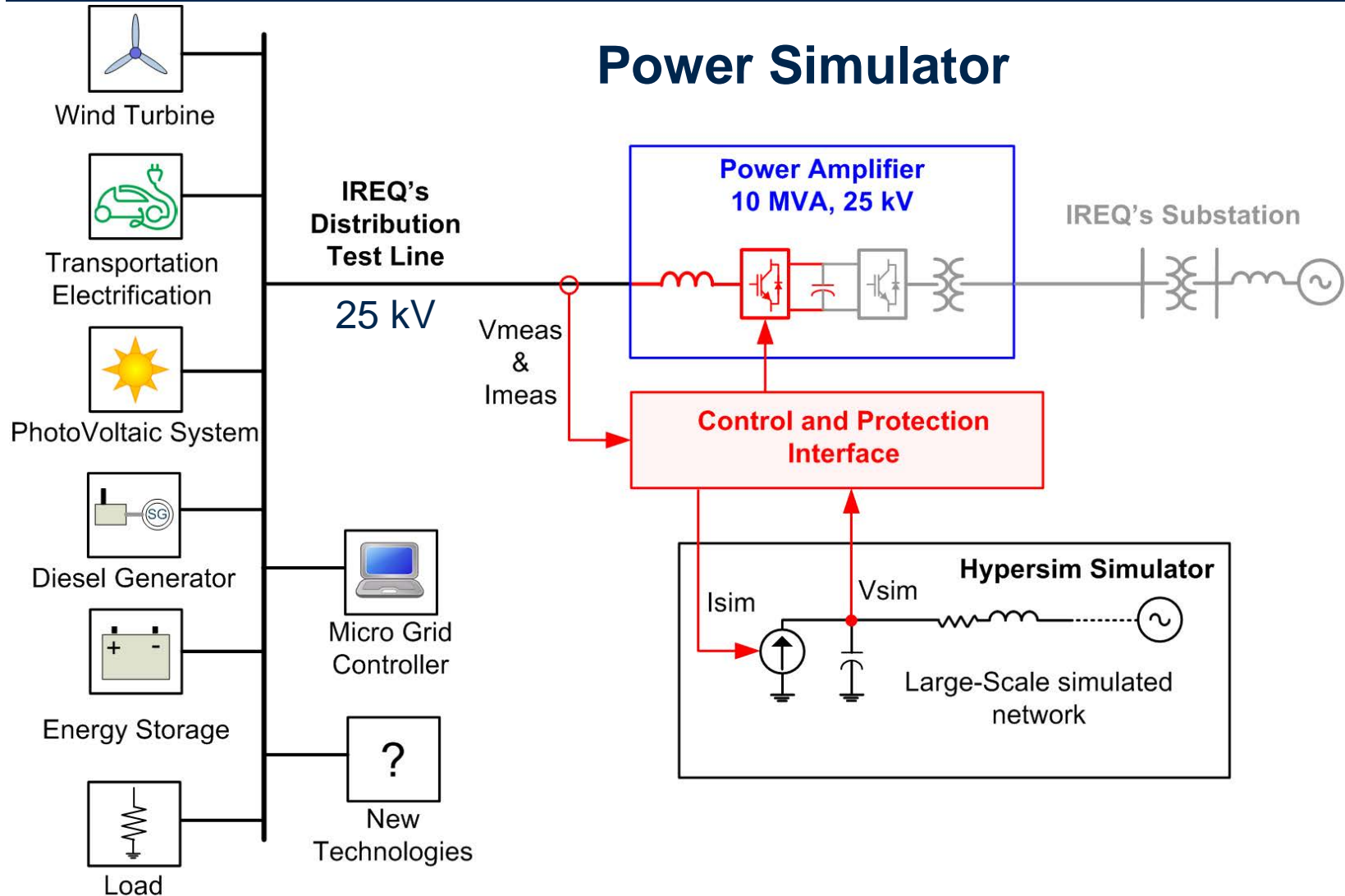
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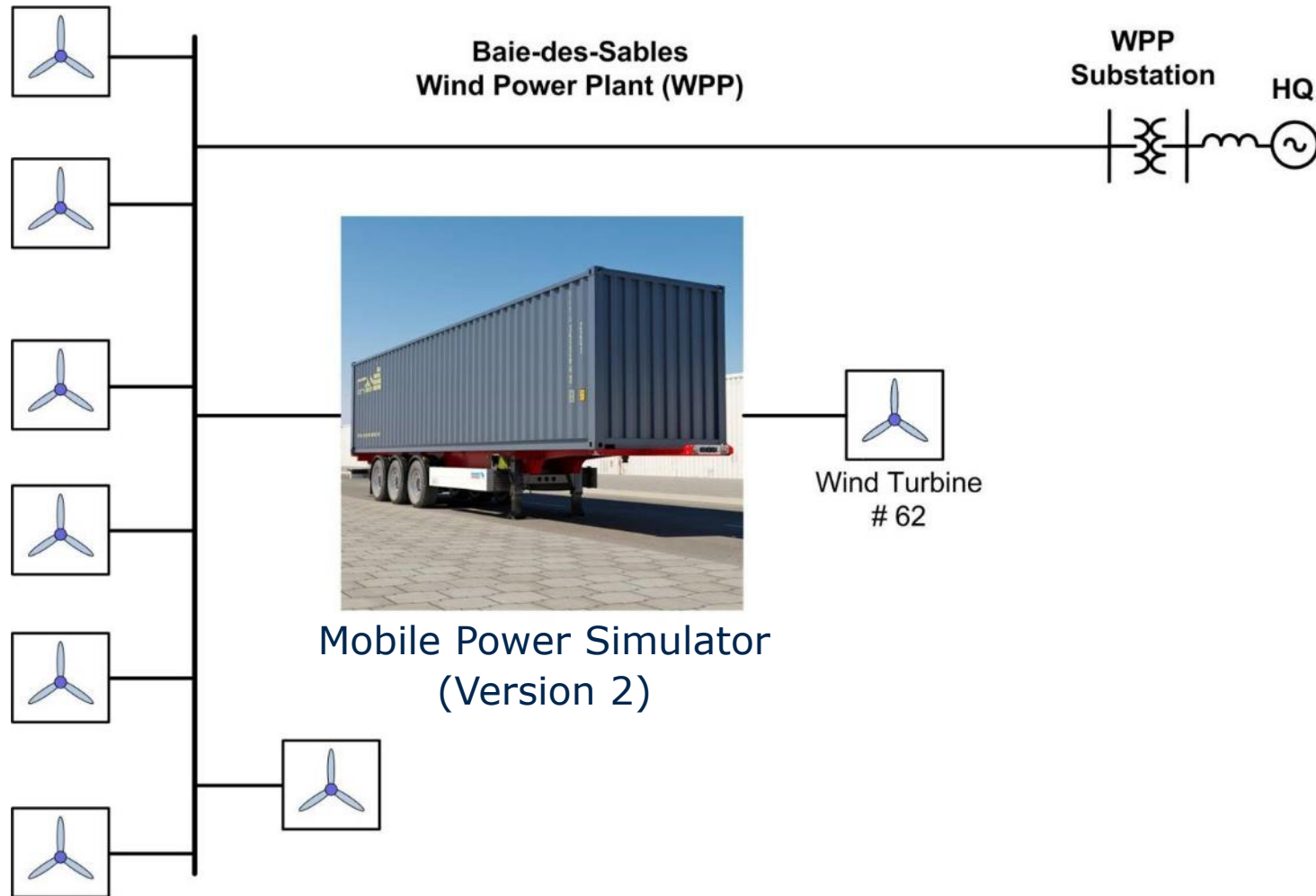
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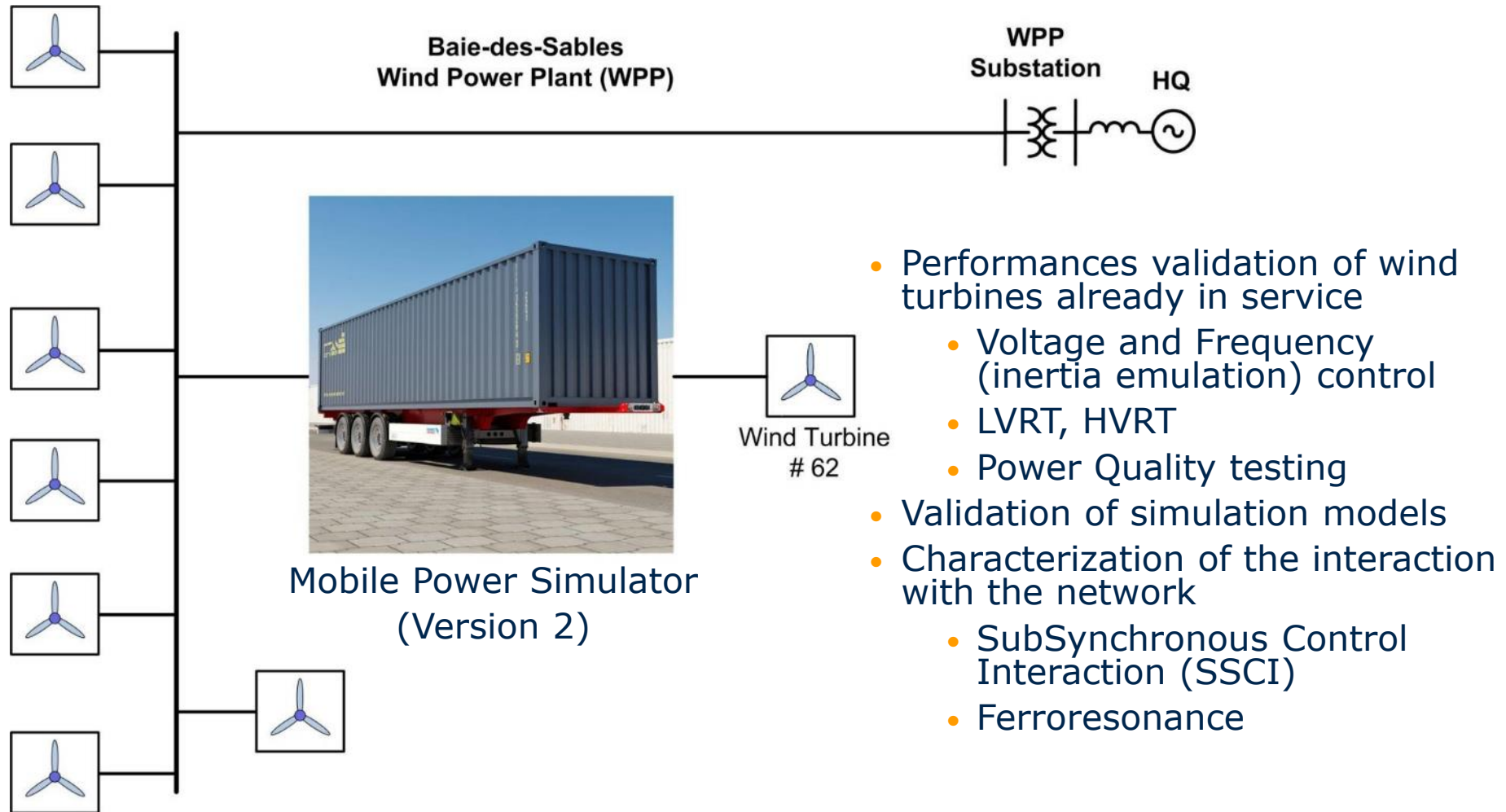
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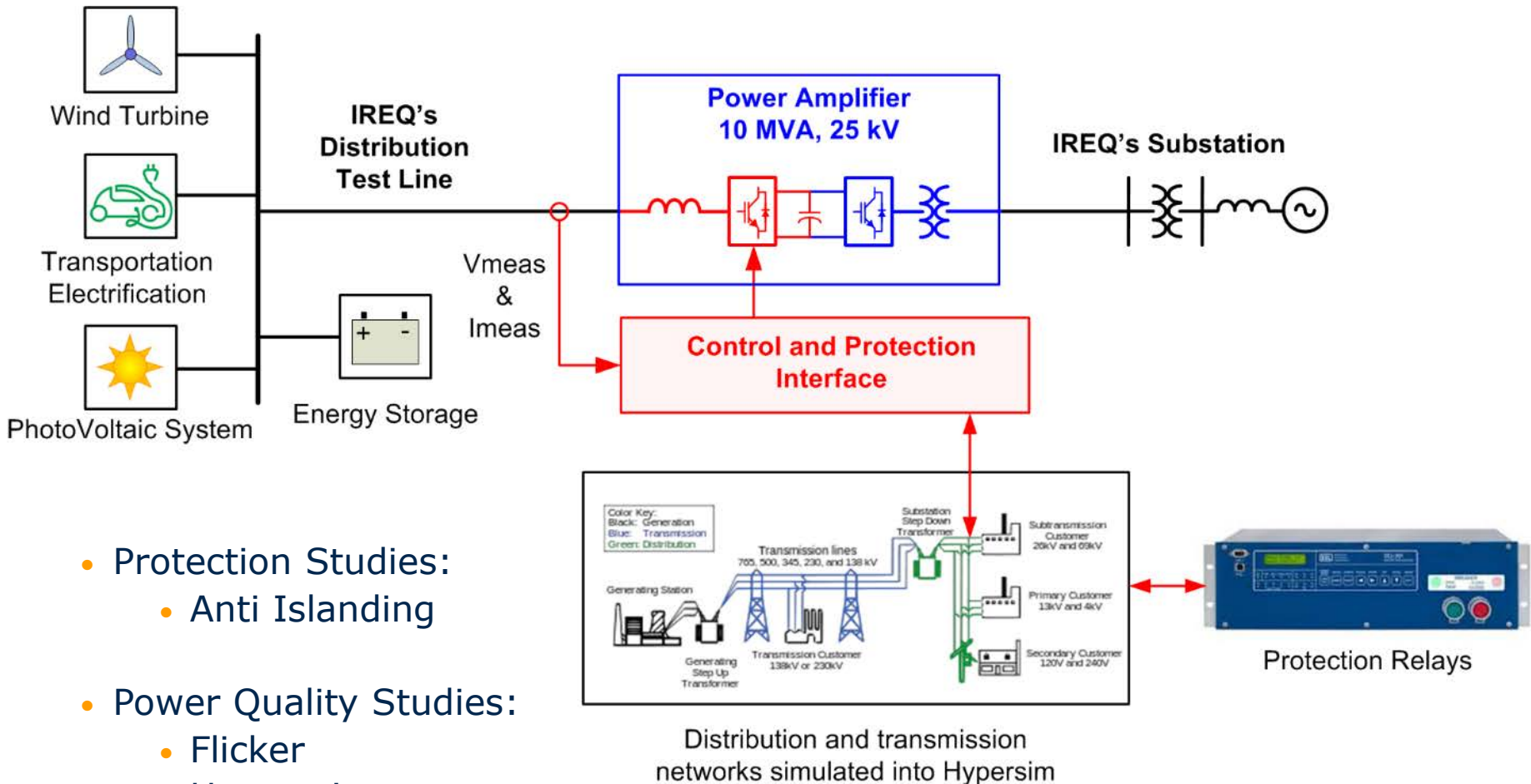
Validation of the performance of actual wind turbines



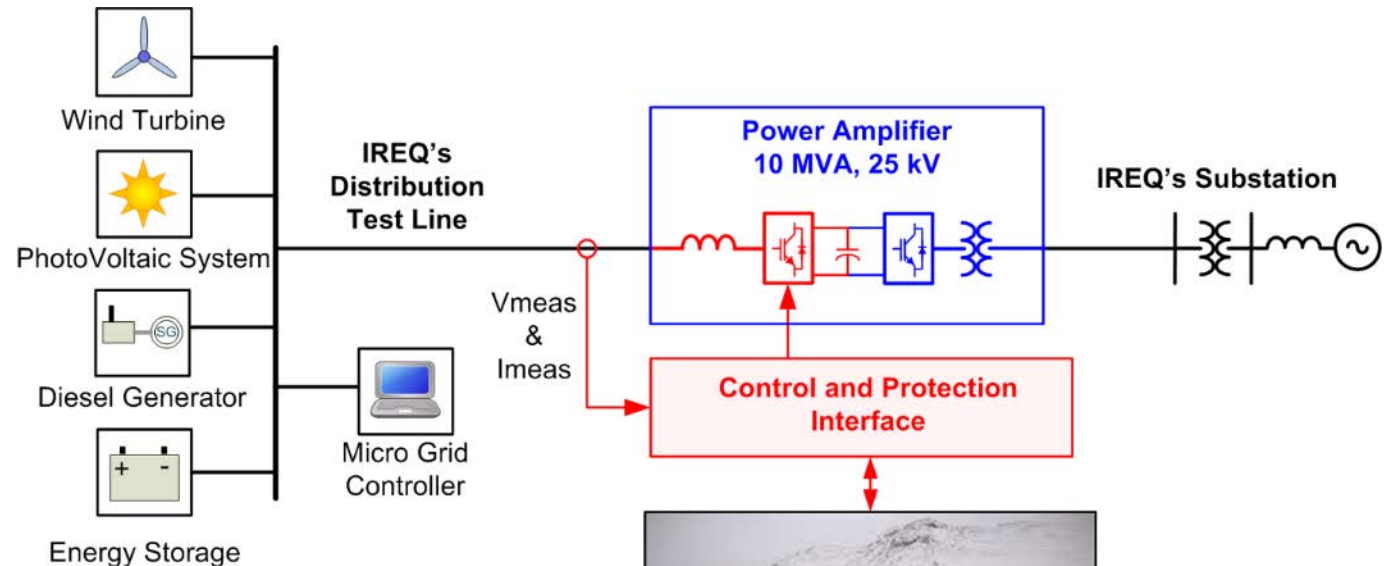
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Integration of renewable energy and storage to distribution networks



Isolated networks



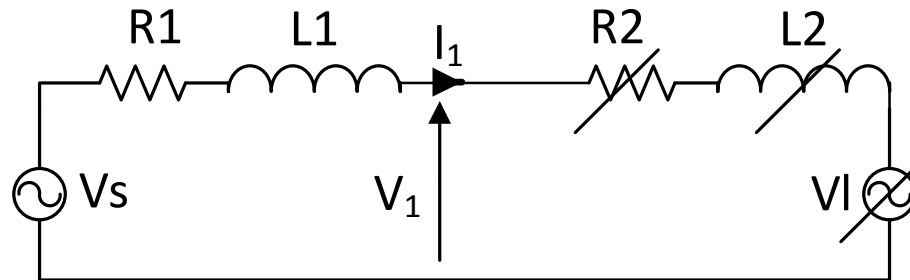
- Development of operating strategies of the integrated system: wind, solar, diesel, energy storage and load
- Performance validation before commissioning
 - Voltage and Frequency Control
 - Power Quality validation



Isolated network simulated into Hypersim

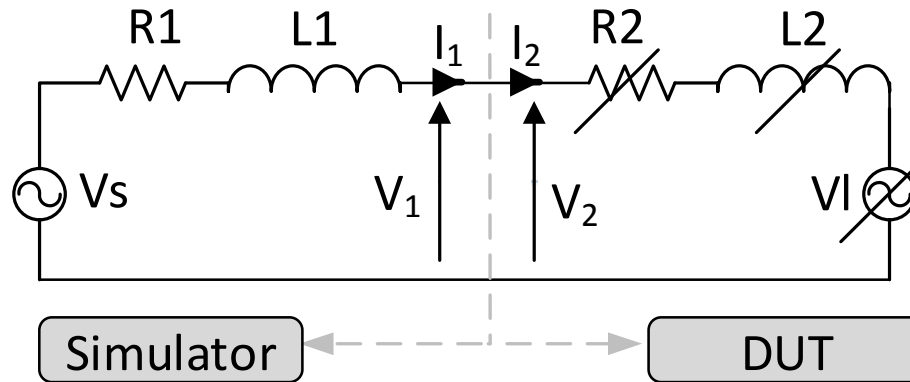
Interface issue

> Original system



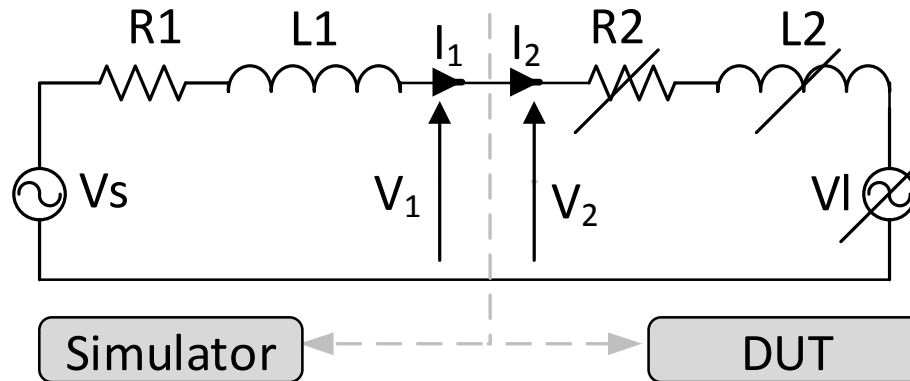
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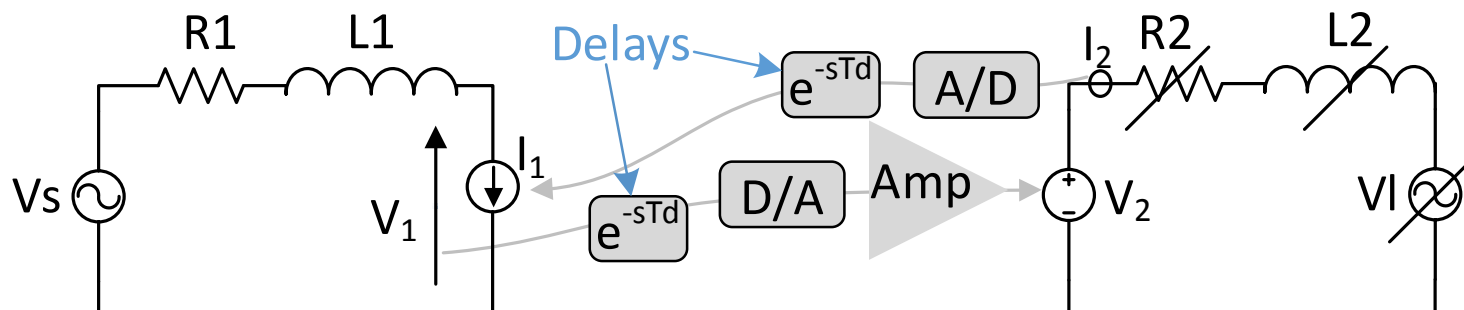


Interface issue

> Original system



> Modified system (decoupled)



Why a prototype?

- > **Before building such equipment, we need to answer some questions:**
 - What kind of controller (FPGA, DSP) ?
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 - What kind of controller (FPGA, DSP) ?
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 - What about the interface between simulator and amplifier?
 - Latency is the work of the devil!!
- > **To answer those questions, we need a flexible reduced scale power amplifier!!**

The features of the prototype

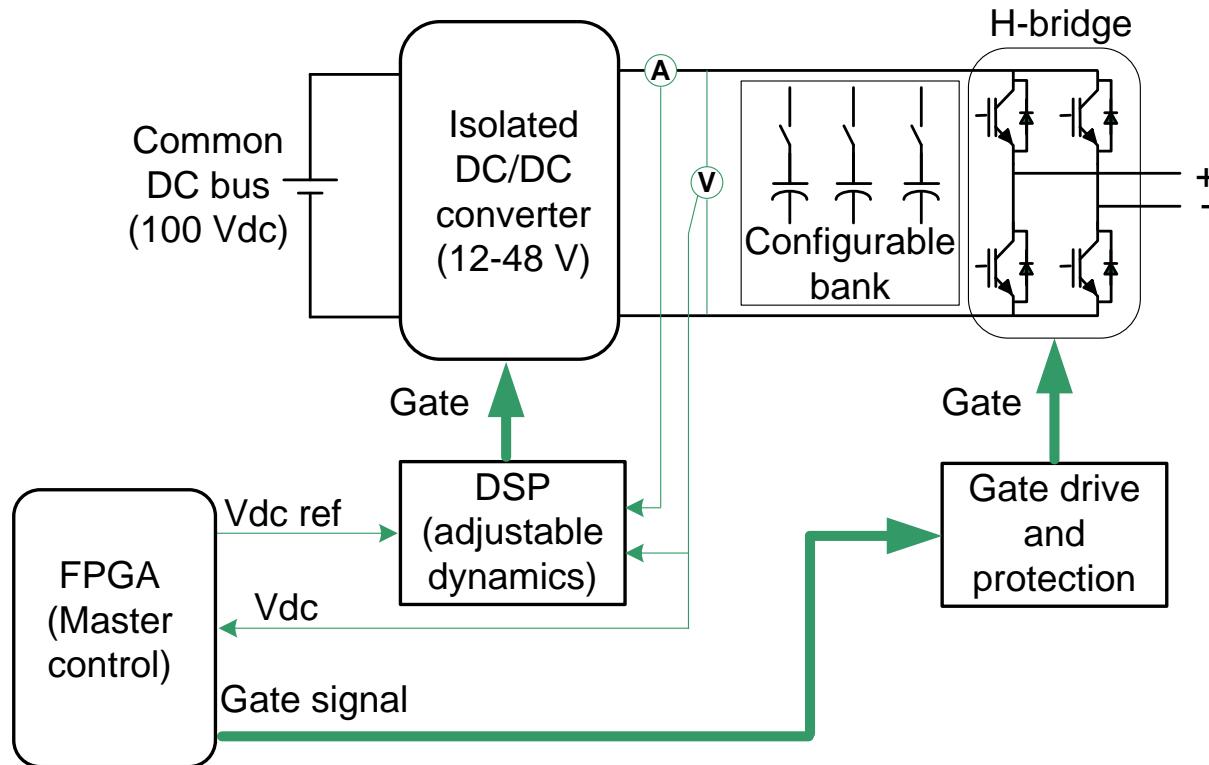
- > Single phase / three-phase
- > Ideal (pure) /real voltage waveform synthesis
- > Adjustable dynamics (to meet full scale constraints)
- > Adjustable number of level
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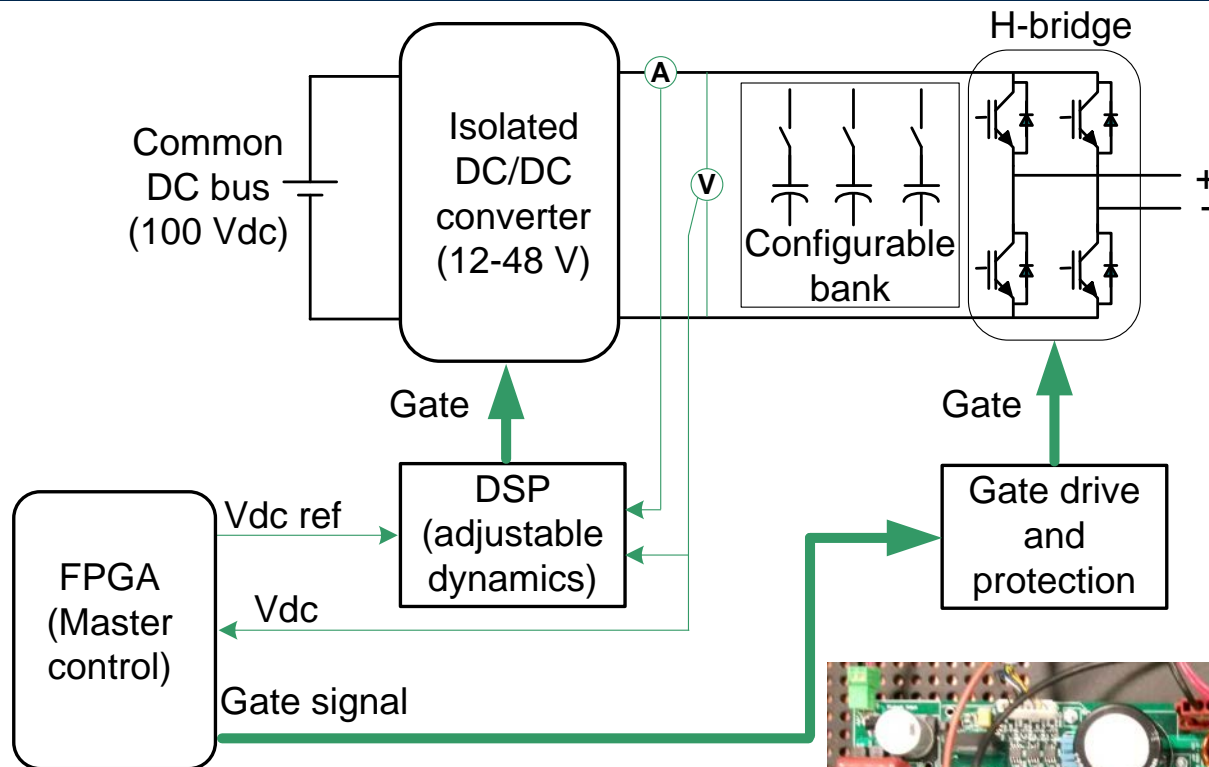
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The solution: a self-powered multi-level converter

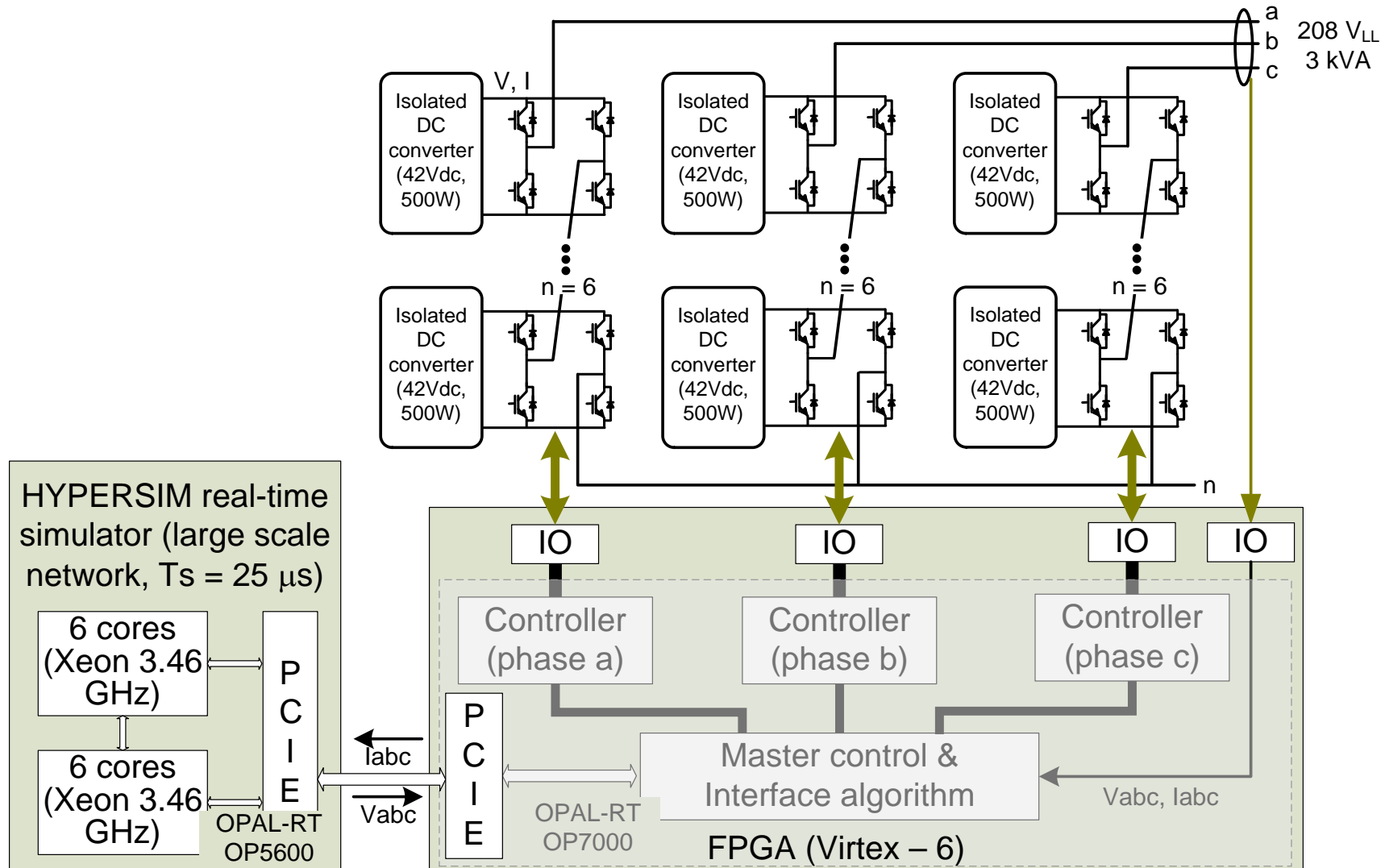
Prototype: the cell



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Prototype: simulator and converter



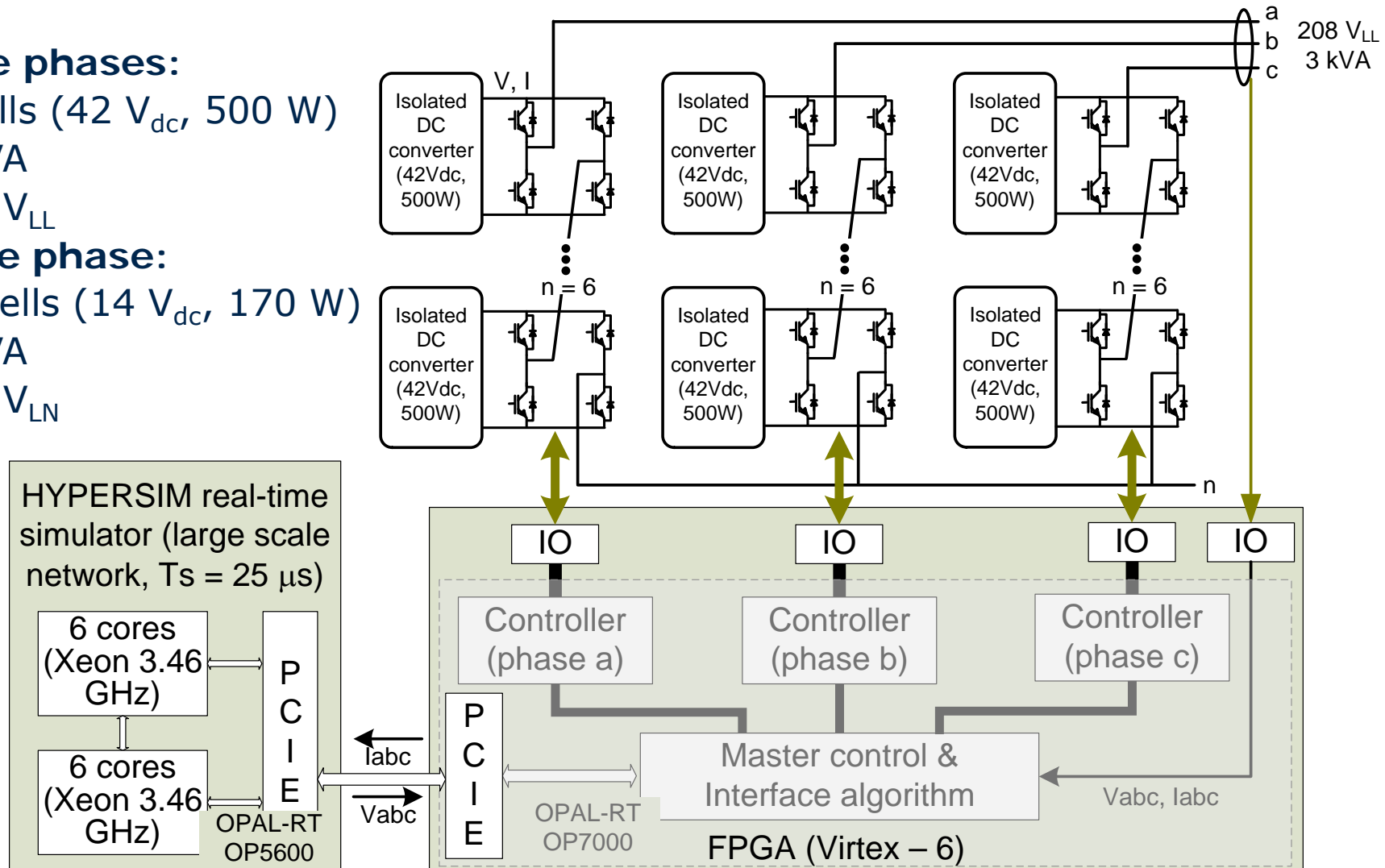
Prototype: simulator and converter

Three phases:

- > 6 cells (42 V_{dc}, 500 W)
- > 3 kVA
- > 208 V_{LL}

Single phase:

- > 18 cells (14 V_{dc}, 170 W)
- > 1 kVA
- > 120 V_{LN}



Conclusion

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> Development of a reduced scale system to:

- Validate the converter controls
- Develop a **stable, robust and high bandwidth interface** between the simulator and the power amplifier

Questions 

